

ETFA 2026

Västerås

Sweden
8-11 September

31st IEEE International Conference on Emerging Technologies and Factory Automation

Call for Papers

Track 2 – Industrial Communication Technologies and Systems

Track chairs

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FOCUS. The track is focused on industrial communication technologies, including tasks such as modeling, analyzing, and synthesizing provably correct systems, as well as characterizing application requirements, implementing solutions, evaluating performance, conducting validation, and case studies.

TOPICS

- ❖ Industrial networks
- ❖ Integration and interoperability of automation networks
- ❖ Middleware and decentralized control for industrial communications
- ❖ IP-based and web-based industrial communications
- ❖ Software-Defined Networks and cognitive radio networks
- ❖ Wireless, sensor, and multi-hop networks
- ❖ Wireless coexistence, 5G/6G and beyond, spectrum-sharing, and radio resource management in industrial systems
- ❖ Advancements in WiFi 7 and WiFi 8 technologies for latency-sensitive industrial applications
- ❖ Time-Sensitive Networking (TSN)
- ❖ Integration of TSN with next-generation wireless networks (5G/6G, WiFi)
- ❖ Quality of Service (QoS) and performance indexes
- ❖ Industrial Internet of Things (IIoT) and Machine-to-Machine (M2M) communications
- ❖ Remote configuration and network management
- ❖ Real-time communication and precise synchronization
- ❖ Event-driven and time-triggered communications
- ❖ Energy efficiency and sustainable industrial communication
- ❖ Artificial Intelligence and Machine Learning for industrial networks

❖ **AIM.** The aim of the conference is to bring together the international community to present the latest research results, share new ideas and engineering breakthroughs, and discuss state-of-the-art challenges and future directions in technology and innovation in the broad domain of Automation with a focus on Industrial and Factory Automation.

❖ **CONFERENCE FORMAT.** The conference will comprise multi-track sessions for regular papers, to present significant and novel research results with a prospect for a tangible impact on the research area and potential implementations, as well as work-in-progress (WiP) and industry practice sessions.

AUTHOR'S SCHEDULE (2026)

Regular and special sessions papers

Submission deadline April 19
Acceptance notification May 25
Deadline for final manuscripts July 4

Work-in-progress/Industry practice papers

Submission deadline May 31
Acceptance notification June 19
Deadline for final manuscripts July 4

Track Program Committee

- ❖ Anna Baron, Friedrich-Alexander-Universität, Germany
- ❖ Anna Friebe, Mälardalen University, Sweden
- ❖ Claudio Zunino, CNR-IEIIT, Italy
- ❖ Dave Cavalcanti, Intel Labs, Oregon, USA
- ❖ Emiliano Sisinni, University of Brescia, Italy
- ❖ Federico Tramarin, University of Padova, Italy
- ❖ Gianluca Cena, CNR-IEIIT, Torino, Italy
- ❖ Henning Trsek, Institute Industrial IT - inIT/TH-OWL, Germany
- ❖ Jetmir Haxhibeqiri, Ghent University, Belgium
- ❖ Joaquim Ferreira, University of Aveiro, Portugal
- ❖ Jonas Peeck, TU Braunschweig, Germany
- ❖ Juergen Jasperte, Fraunhofer IOSB-INA, Germany
- ❖ Julián Proenza Arenas, University of the Balearic Islands, Spain
- ❖ Lisa Maile, Eindhoven University of Technology, The Netherlands
- ❖ Luca Leonardi, University of Catania, Italy
- ❖ Luis Lino Ferreira, Politechnic Institute of Porto, Portugal
- ❖ Lukasz Wisniewski, Institute Industrial IT - inIT/TH-OWL, Germany
- ❖ Luxi Zhao, Beihang University, China
- ❖ Manuel Cheminod, CNR-IEIIT, Torino, Italy
- ❖ Marisol García Valls, Universitat Politècnica de València, Spain
- ❖ Óscar Adamuz-Hinojosa, Universidad de Granada, Spain
- ❖ Ognjen Dobrijevic, ABB Corporate Research, Sweden
- ❖ Paolo Ferrari, University of Brescia, Italy
- ❖ Paulo Portugal, University of Porto, Portugal
- ❖ Ramez Daoud, The American University in Cairo, Egypt
- ❖ Roman Obermaisser, University of Siegen, Germany
- ❖ Silviu Craciunas, TTTech Labs, Austria
- ❖ Stefano Scanzio, CNR-IEIIT, Torino, Italy
- ❖ Stefano Vitturi, CNR-IEIIT, Padova, Italy
- ❖ Valerio Frasca, Intel Labs, Germany

